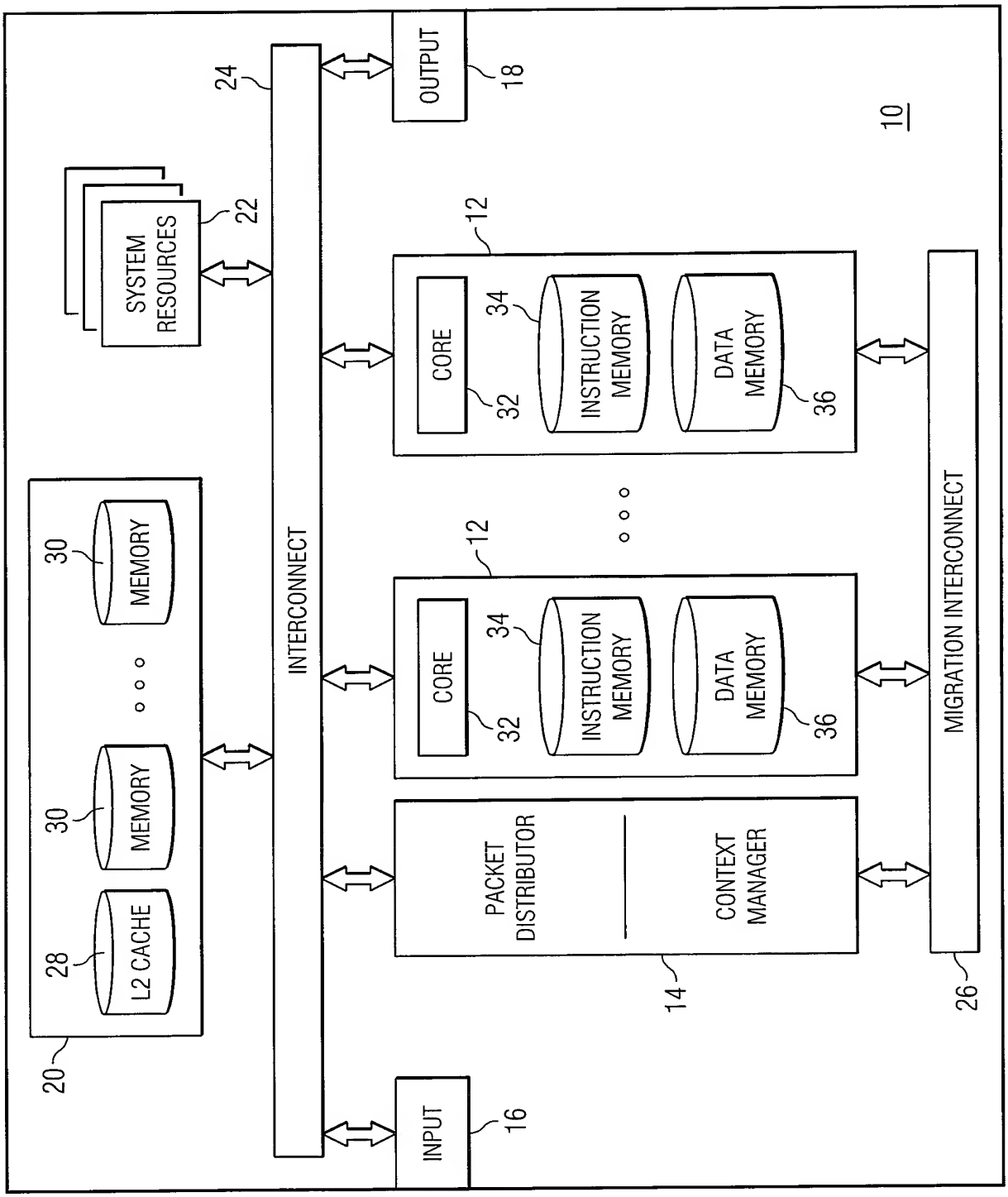
FIG. 1


10
OUTPUT
18
24
SYSTEM RESOURCES
22
12
CORE
32
34
INSTRUCTION MEMORY
DATA MEMORY
36
INTERCONNECT
MEMORY
30
L2 CACHE
28
20
PACKET DISTRIBUTOR
CONTEXT MANAGER
14
MIGRATION INTERCONNECT
26
INPUT
16